

Description

Design and build an op-amp amplifier circuit with a voltage gain (A_V) that is within tolerance of the gain specified.

Given conditions

$V_{in} =$ Inverting
 A_V (ratio) = Non-inverting
 Tolerance $_{A_V} =$

Schematic *Show all component values!*

Parameters

	Measured		Calculated	
V_{in}	<input style="width: 100%;" type="text"/>	A_V (ratio)	<input style="width: 100%;" type="text"/>	
V_{out}	<input style="width: 100%;" type="text"/>	Error $_{A_V}$	<input style="width: 100%;" type="text"/>	$\frac{A_{V(actual)} - A_{V(ideal)}}{A_{V(ideal)}} \times 100\%$